

Sepand Haghghi



Gender: Male

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Birth Date: 9 March 1993

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Marital Status: Single

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Place Of Birth: Rasht,Iran

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Education :

2015-Present Master of Science in Computer Engineering, Sharif University of Technology, Tehran, Iran.

2011-2015 Bachelor of Science in Electronic Engineering, Amirkabir University of Technology, Tehran, Iran. (Credits : 140/140 , Last Year GPA : 3.36/4 , Last Two Year GPA : 3.28/4)

Advisor : [Dr.Majid Shalchian](#)

Thesis : Design a Logic Recommender System For Optimization Of Digital CMOS Integrated Circuits

2010-2011 Pre-University Degree, [MirzaKoochakkhan High School](#), under the supervision of NODET

(National Organization for Developing Exceptional Talents), Rasht, Iran. GPA: 19.40/20

2007-2010 High School Diploma majoring mathematics, [MirzaKoochakkhan High School](#), National Organization for Developing Exceptional Talents, Rasht, Iran. GPA: 18.40/20

Other Education :

2014-2015 Data Science Specialization on Coursera By Johns Hopkins University (Credits : 9/9 , Total Grade : 95.8/100 (3.78/4))

Instructors : [Dr.Roger D. Peng](#) - [Dr.Jef Leek](#) – [Dr.Brian Caffo](#)

Research Interest:

-Robotic

-Machine Learning

-Digital Electronic

-Neural Network

- Embedded System Design

Awards and Honors:

2009 Achieving the 3rd Place in 11th Khwarizmi Youth Award,Field of Electrical and Electronic Inventions,Awarded by President,Ministry of Science,Research and Technology and Ministry of Education,Tehran,Iran

2009 Achieving the 4th Place in 11th Khwarizmi Youth Award,Field of Electrical and Electronic Inventions,Awarded by President,Ministry of Science,Research and Technology and Ministry of Education,Tehran,Iran,

2009 Achieving the 5th Place in Junior Soccer 1 * 1 League,IranOpen2009,Iran,Qazvin,With MirzaKoochakkhan High School Team,"DIZ "

2008 Achieving the 3rd Place in "Fekre Bartar" Guilan Science and Technology Park Idea and Invention Competition,Ministry of Science,Research And Technology

2007 Achieving the 10th Place in Junior Extra Path Finder League,IranOpen2009,Iran,Tehran,With MirzaKoochakkhan High School Team,"Speedo"

2006 Achieving the 10th Place in Junior Extra Path Finder League,Helli Cup,Iran,Tehran,With MirzaKoochakkhan High School Team,"Speedo"

2006 Achieving the 1rd Place in Junior Extra Path Finder League,Helli Robosaz,Iran,Tehran,With MirzaKoochakkhan High School Team,"Speedo"

Publications:

Papers:

Reza Vatani, Pooyan Piredeir, Sepideh Rahmatinia, Sepand Haghghi, Seyedeh Reyhaneh Kammali, "[Damash 2013 Soccer 2D Simulation Team Description Paper](#)" RoboCup Symposium, 2013

Sepand Haghghi, Maoud Deldar Moghadam, "Connect four 1-Wire Temperature Sensors To 8Bit Microcontroller And Delay Approximation " (Unofficial)

Patents:

"Hospital Intelligence Management System", Registration No.:87986, Pub Date:May,20,2015, Data of Filing:Jul,10,2014(Registered in Iran, Certificate Of Grant of Patent)

"Smart Pen", Registration No.:59831, Pub Date:May,12,2011, Data of Filing:Jul,3,2010(Registered in Iran, Certificate Of Grant of Patent)

"Optical Egg Tester", Registration No.:59214, Pub Date:Jun,15,2009, Data of Filing:May,10,2009(Registered in Iran, Certificate Of Grant of Patent)

"AVR Wireless Programmer With Ability Of Encoding", Registration No.:59831, Pub Date:Jul,20,2009, Data of Filing:Jun,25,2009(Registered in Iran, Certificate Of Grant of Patent)

Presentation:

Fall 2015 "Multimedia Signals Semantic Processing", Multimedia Course Final Presentation

Winter2014 "Thermoelectric Cooling Methods and Smart Wall Design", Research and Report Methods Final Presentation

Other Projects:

Dec 2015 `pylist` (New Python Package for Generating music playlist) [[Package Page](#)]

Oct 2015 `qpage` (New Python Package for Generating Simple Academic Homepage in 3-min) [[Package Page](#)]

Summer2015 “Design a microcontroller based circuit for line of sight semiconductor laser communication system”

Jun 2015 Developing New Error Detection Package in Python (Data Transmission Course Final Project) [[Package Page](#)]

Winter2014 “Design A Full-Adder by ULPFA in L-Edit”, VLSI System Design Final Project (Best Project)

Fall 2014 “Design and Implementation of a BCD-ADDER in Proteus “, Logic Circuits and Digital Design Final Project

Fall 2015 “Implementation of Horn-Schunck and Lucas Kanade Algorithm for Optical Flow In Matlab”, Multimedia Systems Final Project

Skills:

Engineering Softwares: Matlab(Octave)-OrCAD-Altium Designer-Proteus

Programming Languages: Python – Ruby – C/C++ - Basic-R-VHDL-Verilog-CSS-JavaScript-HTML

IDE : Visual Studio – RAD Studio – Bascom – MPLAB – MicroCode Studio – Keil

General Softwares: Microsoft Office (Word, PowerPoint, Excel)